

INCH-POUND

MIL-M-38510/508
AMENDMENT 1
20 February 1992

MILITARY SPECIFICATION
MICROCIRCUITS, MEMORY, DIGITAL, CMOS
ONE-TIME PROGRAMMABLE ARRAY LOGIC
MONOLITHIC SILICON

This amendment forms a part of MIL-M-38510/508, dated 15 November 1990, and is approved for use by all Departments and Agencies of the Department of Defense.

PAGE 7

Table I, input capacitance, max column: Delete "8" and substitute "10". Also, output capacitance, max column: Delete "8" and substitute "10".

PAGE 8

Table I, clock pulse width (t_{WH}): Add "4/" in parameter column. Also, clock pulse width (t_{WL}): Add "4/" in parameter column.

PAGE 9

Table I, setup time, device type 04, min column: Delete "10" and substitute "12". Also, maximum clock frequency, device type 04, min column: Delete "50" and substitute "45".

CONCLUDING MATERIAL

Custodians:

Army - ER
Navy - EC
Air Force - 17

Review activities:

Army - AR, MI
Navy - OS, SH, TD
Air Force - 19, 85, 99
DLA - ES

User activities:

Army - SM
Navy - AS, CG, MC

Preparing activity:

Air Force - 17

Agent:

DLA - ES

(Project 5962-1253)